

Refine Search

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L28 and L25	1

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L30

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Search History

DATE: Monday, September 27, 2004 [Printable Copy](#) [Create Case](#)

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result set

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

<u>L30</u>	L28 and I25	1	<u>L30</u>
<u>L29</u>	L28 and I17	1	<u>L29</u>
<u>L28</u>	(first processor core\$1) and (second processor core\$1)	37	<u>L28</u>
<u>L27</u>	L25 and I9	1	<u>L27</u>
<u>L26</u>	L25 and I14	1	<u>L26</u>
<u>L25</u>	(concurrent\$4 near2 execut\$5) near3 (thread\$1 or program\$1)	1446	<u>L25</u>
<u>L24</u>	(concurrent\$4 near2 execut) near3 (thread\$1 or program\$1)	0	<u>L24</u>
<u>L23</u>	L18 and I9	2	<u>L23</u>
<u>L22</u>	L19 and I9	0	<u>L22</u>
<u>L21</u>	L20 and I9	8	<u>L21</u>
<u>L20</u>	709/\$.ccls.	31180	<u>L20</u>
<u>L19</u>	717/\$.ccls.	7062	<u>L19</u>
<u>L18</u>	714/\$.ccls.	46407	<u>L18</u>
<u>L17</u>	redundancy check\$5 mode\$1	26	<u>L17</u>

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<u>L16</u>	L15 and l14	3	<u>L16</u>
<u>L15</u>	detect\$5 near2 (error\$1 or fault\$1)	176461	<u>L15</u>
<u>L14</u>	(first processor core\$1) same (second processor core\$1)	36	<u>L14</u>
<u>L13</u>	generat\$5 near3 interrupt\$1	23483	<u>L13</u>
<u>L12</u>	L7 and l8	4	<u>L12</u>
<u>L11</u>	L9 and l8	15	<u>L11</u>
<u>L10</u>	L9 and l9	65	<u>L10</u>
<u>L9</u>	l2 same l3	65	<u>L9</u>
<u>L8</u>	(virtual\$4 near3 (eject\$5 or removal\$1)) near3 device\$1	71	<u>L8</u>
<u>L7</u>	service\$5 near3 event\$1	9284	<u>L7</u>
<u>L6</u>	L5 same 4	0	<u>L6</u>
<u>L5</u>	(virtual\$4 near3 eject\$5) near3 device\$1	29	<u>L5</u>
<u>L4</u>	(device\$1 removal) near3 sequence\$1	8	<u>L4</u>
<u>L3</u>	device\$1 near3 remov\$5	243267	<u>L3</u>
<u>L2</u>	(detect\$5 near3 event\$1) same process\$6	11151	<u>L2</u>
<u>L1</u>	(detect\$5 enar3 event\$1) same process\$6	0	<u>L1</u>

END OF SEARCH HISTORY

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